



# Ice Lake Platform Intel® IOM Firmware

**Release Notes - NDA**

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***Revision 04.011.0.00***

***November 2019***

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# Contents

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1	Introduction .....	5
	1.1 Scope of Document .....	5
	1.2 Acronyms .....	5
2	Issue Status Definitions .....	6
3	Intel® IOM New Features - RCR's .....	7
4	Closed Issues .....	8
5	Open / Known Issues – To Date .....	9
6	Archive .....	11
	6.1 Fixed in Previous Kits .....	11
	6.2 RCR's .....	11



## Revision History

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Revision Number	Description	Revision Date
04.001.0.00	Initial release	Jan 2019
04.002.0.00	ES2 Supp release	Feb 2019
04.004.0.00	QS first Release	March 2019
04.007.0.00	QS Release	April 2019
04.008.0.00	Release	May 2019
04.009.0.00	Release	May 2019
<del>04.00A.0.00</del>	<del>Release</del>	<del>June 2019</del>
04.00B.0.00	Release	June 2019
04.00C.0.00	PV Release	June 2019
04.00D.0.00	PR1 release	July 2019
04.00E.0.00	PR1 release	July 2019
04.00F.0.00	PR2 release	September 2019
04.010.0.00	PR2 release	October 2019
04.011.0.00	PR2 release	November 19

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# 1 Introduction

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## 1.1 Scope of Document

This document provides component level details of the downloaded kit and the contents of each folder in the kit.

## 1.2 Acronyms

Term	Description
BIOS	Basic Input Output System
FW	Firmware

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## 2 *Issue Status Definitions*

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This document provides sightings and bugs report for Intel® CSME Firmware 13.0 SKU for the Ice Lake Platform. At the time of a milestone release, this report will be distributed with the Intel® CSME Kit and will provide information on new issues and the status of old issues (replacing the Release Notes document).

**Closed Issues:** This category will only display closed issues within the current Intel® CSME Kit release. After each release, old issues will be dropped down to the “Archive” section and then new closed issues will take its place back up top for the next release. If an issue is posted in this section, it will indicate that the issue has been verified and fixed within the kit that is being released.

**Known Issues:** This category will display all Known Issues since the Alpha release and will remain in this section until fixed or noted otherwise. “Known Issues” are still under investigation and may or may not be root caused.

**Archive – Fixes in Previous Kits:** This category will display all closed issues that were closed in their respected kit#. This section will serve as a history of fixed issues.

**Sightings listed in this document apply to the Ice Lake SKU’s unless noted otherwise in this document or in the sightings tracking systems.**

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### 3     *Intel® IOM New Features - RCR's*

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RCR Number	Change Information

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## 4 Closed Issues

Issue Number	Description	Details	Fixed in Version
1408850882	CATERR may be observed during Thunderbolt device plug/unplug event with enabled Thunderbolt RTD3 and TCcold	CATERR may be observed while connecting a Type-C device when the Thunderbolt Controller is in TC Cold state	04.002.0.00
1408790507	Sporadic CATERR may occur while the processor enters an Sx state when PCIe and Thunderbolt are enabled	While Thunderbolt enables clock gating, PCIe may sporadically fail to respond Sx entry requests from IOM and may cause a system hang.	04.002.0.00
1408788592	Warm reset with Thunderbolt Device connected may cause system hang.	After warm reset exit when Thunderbolt device connected, processor may not respond to PCH request and prevent from PCH to send the next request.	04.002.0.00
2206840222	Sporadic Hang may occur while the processor enters S3 state when a USB device is connected	While entering S3 state after wake from PKGC10, XHCI controller may fail to answer S3 state request and may cause the system to hang.	04.004.0.00
1306256007	Presented monitors may be switched after Sx alive exit	While connecting 3 monitors to 2 adjacent ports (2 TBTs and 1 DP alt_mode) only first 2 monitors will be presented. When going to Sx alive the presented monitors at exit may not be the first that was presented.	04.004.0.00
1408752950	Sporadic CATERR may occur while TCSS enters TC7 state when connecting USB3 device	Under certain condition XHCI controller may fail to answer PM_RSP on time and may cause hang.	04.007.0.00
1409253887	System may hang after a few iterations of enter/exit from TC7 state	While IOM sends sideband request, MGPHY may sporadically fail to respond and cause a system hang.	04.007.0.00
1606693868	USB device may fail to enumerate after connection	Sporadic link enumeration failures may observed while connecting USB device due to XHCI self power gate	04.008.0.00
1306458545	TCSS may hang while connect/disconnect external display to the same port.	Under certain condition external display connect/disconnect may fail to send HPD de-assert nor disconnect messages	04.009.0.00
1409419957	Sporadic CATERR may occur while the processor enters PkgC10 when PCIe and Thunderbolt are disabled	TCSS may hang while entering TC6/7 since MGphy fail to respond reset assertion	04.00B.0.00



**Closed Issues**



Issue Number	Description	Details	Fixed in Version
1409479077	While exiting from connected standby mode the TBT connected monitors may not wake up.	TBT screens may not wake up While exiting from connected standby mode since TBT controller fail to send HPD assert.	04.00B.0.00
1409512819	TCSS USB Host Controller (xHCI) May Hang	TCSS USB Host Controller (xHCI) may hang when a USB 3.x Device requests U2 exit and the xHCI controller is entering autonomous power gated state (d0i2) asynchronous occur at the same time.  A workaround disabling d0i2.	04.00C.0.00
1409492291	IOM does not support fixed Display connection with TCSS disabled	Processor may hang while connecting display to fixed Display port with XHCI disabled	04.00D.0.00
1409617407	IOM does not support TCCOLD in fixed DP mode when connection is not present		04.00E.0.00
1408752950	Unpopulated Type-C to Type-B or Type-A Converter (Cable or Dongle) May Degrade Type-C Port Functionality		04.00F.0.00
1306809558	USB Receiver, may not enter to compliance mode	USB Receiver, entry improvement to Compliance mode	04.010.0.00
14010260616	USB Gen2 devices may observed link stability after Hot Plug		04.011.0.00



## **5      *Open / Known Issues – To Date***

<b>Issue Number</b>	<b>Description</b>	<b>Details</b>	<b>Fix status</b>

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## 6 Archive

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### 6.1 Fixed in Previous Kits

N/A

### 6.2 RCR's

N/A

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